What is claimed is:-

- 1. A transceiver interface connectable, in use, to a cable, the interface including an input for receiving a pair of signals from said cable, and a logic circuit connected to the input having a single logic output line, wherein the logic circuit has a first output state where both signals are below a predetermined level and a second output state where either or both signals exceed the predetermined level.
- 10 2. An interface as claimed in Claim 1, wherein the logic circuit is a NOR gate.
 - 3. An interface as claimed in Claim 1 or Claim 2, wherein the first output state is indicative of a single ended zero state.

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- 4. An interface as claimed in any preceding Claim, wherein the second output state is indicative of a non-single ended zero state.
- 5. A serial bus device including a transceiver interface as claimed in any one of Claims 1 to 4.
 - 6. A device as claimed in Claim 5, wherein the device includes an application specific integrated circuit having a single physical connection to said logic output line.

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- 7. A device as claimed in Claim 5 or Claim 6, wherein the bus is Universal Serial Bus.
- 8. A mobile radio telephone including a device as claimed in Claim 6 or Claim 7 as dependent therefrom, wherein the application specific integrated circuit also provides mobile telephony functions.

9. A transceiver interface connectable, in use, to a cable substantially as described herein with reference to Figure 5 and Figure 6 of the accompanying drawings.